

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions,
and listings, of claims in the application:

LISTING OF CLAIMS:

1. (currently amended) A liquid crystal display device
comprising:

pixel electrodes;

a common electrode;

a plurality of data lines and a plurality of gate lines
intersecting each other;

B/ a plurality of switchers, provided for the pixel
electrodes, for supplying signals from the data lines to the
pixel electrode;

a gate line driver for scanning the gate lines;

a data line driver for driving the data lines, in
accordance with the gradation to be displayed; and

a controller for controlling the gate line driver and
the data line driver, wherein

the controller comprises a signal absence detector for
detecting that at least one of a video signal, a horizontal
synchronization signal, and a vertical synchronization signal is
no longer being input to the liquid crystal display device,
[[and]]

wherein, in response to a detection by the signal absence detector that one of the signals is no longer being input, the controller outputs a signal to the gate line driver to make all the gate lines active for a predetermined time and the controller outputs a signal to the data line driver to supply the same electric potential as applied to the common electrode to all the data lines for the predetermined time, and

wherein the predetermined time is determined based on a time constant of a resistor and a capacitor.

2. (original) A liquid crystal display device according to claim 1, wherein the predetermined time is a time required to discharge all the charge from the liquid crystal by supplying the common electric potential to all the pixel electrodes.

3. (canceled)

4. (original) A liquid crystal display device according to claim 1, further comprising a power supply maintaining circuit for maintaining power after a power supply to the liquid crystal display device is turned off.

5. (original) A liquid crystal display device according to claim 1, wherein the data line driver connects all the data lines to the ground after a power supply to the liquid crystal display device is turned off.

6. (canceled)

B/ 7. (currently amended) A method for controlling a liquid crystal display device comprising: pixel electrodes; a common electrode; a plurality of data lines and a plurality of gate lines intersecting each other; a plurality of switchers, provided for the pixel electrodes, for supplying signals from the data lines to the pixel electrode; a gate line driver for scanning the gate lines; a data line driver for driving the data lines, in accordance with the gradation to be displayed; and a controller for controlling the gate line driver and the data line driver, the method comprising the steps of:

detecting that at least one of a video signal, a horizontal synchronization signal, and a vertical synchronization signal is no longer being input to the liquid crystal display device; [[and]]

in response to detection that one of the signals is no longer being input to the liquid crystal display device, making all the gate lines active for a predetermined time and supplying the same electric potential as applied to the common electrode to all the data lines for the predetermined time, wherein the predetermined time is determined based on a time constant of a resistor and a capacitor.
